



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/834,660
Filing Date April 12, 2001
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Laura Schillinger
Attorney's Docket No. MI22-1637
Title: Semiconductor Processing Methods Of Forming Transistors, Semiconductor Processing Methods Of Forming Dynamic Random Access Memory Circuitry, and Related Integrated Circuitry

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References – See Attached Form PTO-1449

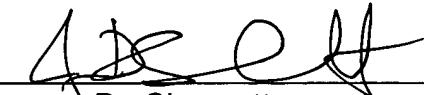
EY372467966

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated: 10/4/04

By: 
James D. Shaurette
Reg. No. 39,833

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Sheet 1 of 1

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)					ATTY. DOCKET NO. MI22-1637	SERIAL NO. 09/834,660		
					APPLICANT Luan C. Tran			
					FILING DATE April 21, 2001	GROUP 2813		
U.S. PATENT DOCUMENTS								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	5,817,567	10/6/1998	Jang et al.				
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
	AQ							
	AR							
	AS							
EXAMINER <small>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>				DATE CONSIDERED				